

10735806\_CLS.txt  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10735806 on April 20, 2005

Original Classifications

3 711/5  
2 365/222  
2 710/110  
2 712/35

Cross-Reference Classifications

4 365/230.03  
4 711/105  
3 711/154  
2 365/206  
2 365/227  
2 365/230.06  
2 365/230.08  
2 710/306  
2 710/52  
2 710/58  
2 711/119  
2 711/155  
2 712/32  
2 712/36  
2 713/323  
2 714/734

Combined Classifications

5 365/230.03  
4 711/105  
3 365/230.06  
3 710/52  
3 711/154  
3 711/5  
3 712/32  
3 713/323  
2 365/201  
2 365/203  
2 365/206  
2 365/222  
2 365/227  
2 365/230.08  
2 710/110  
2 710/22  
2 710/306  
2 710/58  
2 711/119  
2 711/155  
2 711/169  
2 712/35  
2 712/36  
2 713/322  
2 714/30  
2 714/734

Titles of Most Frequently Occurring Classifications of Patents Returned  
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- 5 365/230.03 (1 OR, 4 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/230.03 .Plural blocks or banks
- 4 711/105 (0 OR, 4 XR)  
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY  
711/100 STORAGE ACCESSING AND CONTROL  
711/101 .Specific memory composition  
711/104 ..Solid-state random access memory (RAM)  
711/105 ...Dynamic random access memory
- 3 365/230.06 (1 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/230.06 .Particular decoder or driver circuit
- 3 710/52 (1 OR, 2 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/52 .Input/Output data buffering
- 3 711/154 (0 OR, 3 XR)  
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY  
711/100 STORAGE ACCESSING AND CONTROL  
711/154 .Control technique
- 3 711/5 (3 OR, 0 XR)  
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY  
711/1 ADDRESSING COMBINED WITH SPECIFIC MEMORY  
CONFIGURATION OR SYSTEM  
711/5 .For multiple memory modules (e.g., banks,  
interleaved memory)
- 3 712/32 (1 OR, 2 XR)  
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION  
PROCESSING  
712/1 PROCESSING ARCHITECTURE  
712/32 .Microprocessor or multichip or multimodule  
processor having sequential program control
- 3 713/323 (1 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/300 COMPUTER POWER CONTROL  
713/320 .Power conservation  
713/323 ..Active/idle mode processing
- 2 365/201 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/201 .Testing
- 2 365/203 (1 OR, 1 XR)

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Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/203 .Precharge

2 365/206 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/206 .Noise suppression

2 365/222 (2 OR, 0 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/222 .Data refresh

2 365/227 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/226 POWERING
365/227 .Conservation of power

2 365/230.08 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.08 .Including particular address buffer or latch
circuit arrangement

2 710/110 (2 OR, 0 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS
TRANSACTION PROCESSING)
710/107 .Bus access regulation
710/110 ..Bus master/slave controlling

2 710/22 (1 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/22 .Direct Memory Accessing (DMA)

2 710/306 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS
TRANSACTION PROCESSING)
710/305 .Bus interface architecture
710/306 ..Bus bridge

2 710/58 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/58 .Input/output process timing

2 711/119 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/119 ...Multiple caches

2 711/155 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING

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                                SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/154    .Control technique
711/155    ..Read-modify-write (RMW)

2  711/169    (1 OR, 1 XR)
    Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: MEMORY
711/100    STORAGE ACCESSING AND CONTROL
711/167    .Access timing
711/169    ..Memory access pipelining

2  712/35     (2 OR, 0 XR)
    Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
                PROCESSING
712/1      PROCESSING ARCHITECTURE
712/32     .Microprocessor or multichip or multimodule
                processor having sequential program control
712/34     ..Including coprocessor
712/35     ...Digital signal processor

2  712/36     (0 OR, 2 XR)
    Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
                PROCESSING
712/1      PROCESSING ARCHITECTURE
712/32     .Microprocessor or multichip or multimodule
                processor having sequential program control
712/36     ..Application specific

2  713/322    (1 OR, 1 XR)
    Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: SUPPORT
713/300    COMPUTER POWER CONTROL
713/320    .Power conservation
713/322    ..By clock speed control (e.g., clock on/off)

2  714/30     (1 OR, 1 XR)
    Class 714 : ERROR DETECTION/CORRECTION AND FAULT
                DETECTION/RECOVERY
714/100    DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
714/1      .Reliability and availability
714/25     ..Fault locating (i.e., diagnosis or testing)
714/27     ...Particular access structure
714/30     ....Built-in hardware for diagnosing or testing
                within-system component (e.g., microprocessor test mode
                circuit, scan path)

2  714/734    (0 OR, 2 XR)
    Class 714 : ERROR DETECTION/CORRECTION AND FAULT
                DETECTION/RECOVERY
714/699    PULSE OR DATA ERROR HANDLING
714/724    .Digital logic testing
714/734    ..Structural (in-circuit test)

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10735806\_QUAL.txt

5699553 99  
4315308 99  
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4458313 99  
4772888 99  
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4864491 99  
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